

Silicon Mobility

Internship Description

Low-level software (Drivers) development
on automotive controller chip
(SM-STC006 / 2023)

What we offer

Company	<p>SILICON MOBILITY SAS (<i>immatriculée 815 085 659 000 28 RCS Grasse</i>)</p> <p><u>Siege social</u> : Les Aqueducs – Bât 2 – 535, route des Lucioles – 06560 Valbonne Sophia-Antipolis</p> <p>The Automotive industry is living a revolution. Electrification, autonomous driving, diverse mobility, connectivity are trends that are drastically changing the industry’s rules. Among all decisive topics revolutionizing cars in the next future, Silicon Mobility is committed to support the rapid advent of electric and hybrid cars.</p> <p>Silicon Mobility is a technology leader for cleaner, safer and smarter mobility. The company designs, develops and sells flexible, real-time, safe and open semiconductor solutions for the automotive industry used to increase energy efficiency and reduce pollutant emissions while keeping passengers safe.</p> <p>We are looking for a good candidate to join our R&D team working in Sophia-Antipolis on the Côte d’Azur. Please contact us: internship2023@silicon-mobility.com</p>
	Offer Ref.
Subject – Offer title	Low-level software (Drivers) development on automotive controller chip
Duration	6 months – between February/March/April and September 2023
Work hours	35 hours a week at Silicon Mobility office
Education	Last year of Master (BAC+5 or equivalent)
Content / Mission	<p>As part of its product roadmap, Silicon-Mobility is developing its new generation of FPCU System-on-Chip. This innovative architectural component is based on a multi-core architecture combined with a patented real-time subsystem including an embedded programmable logic structure.</p> <p>This internship consists in defining and developing several device drivers within the Low-level Software layer. It is a collection of embedded software modules which offer services needed to run the functional part of the upper software layer (system/application).</p> <p>The project contains:</p> <p><u>Introduction</u></p> <ul style="list-style-type: none"> • Analyze and understand the FPCU System-on-Chip and its components. <p><u>Development steps</u></p> <ul style="list-style-type: none"> • Study and understand the module functionalities • Define the software requirements. • Describe the driver software architecture and refine it into a more precise description of the functions to be developed. • Code implementation in embedded C. • Functional and integration tests: definition, development, and execution. This implies SW and HW debug on the target.
Profile required	For this internship, we are looking for a candidate with good knowledge of embedded systems and embedded C programming. RTL simulation and FPGA prototyping will be also an advantage.



	<p>Good skills in hardware design for embedded system would be appreciated. The good candidate will be autonomous, rigorous with a strong team spirit. English speaking is required.</p>
<p>Expected skills / Knowledge</p>	<ul style="list-style-type: none"> • Embedded C coding • Embedded systems knowledge • Real-time software constraints • RTL simulation and FPGA debug • Quality management skills
<p>Remuneration</p>	<p>1400€/month + Tickets Restaurant</p>